مثلا ور والنازها

## Menoufia University

Faculty of Electronic Engineering 2<sup>nd</sup>year. 1<sup>St</sup> Term Exam.

Very large scale Integrated Circuit Technology First PaperTime allowed: 1.5 hours

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- 1. a) Explain wafer pure Silicon three steps method of production?
  - b) What are the fabrication four processes of Poly silicon self-align gate NMOS Tr.? (use color in your explication)
- 2- An n-channel MOS transistor has the following properties:
  - Substrate Doping Concentration, N<sub>A</sub>= 2 x 10<sup>16</sup> cm<sup>-3</sup>
  - Metal Gate Fermi Potential,  $\phi_{FM} = 0.6 \text{ V}$
  - Oxide Concentration, Nox = 10<sup>10</sup> cm<sup>-2</sup>
  - \* Free Space Permittivity=8.85x10<sup>-14</sup> F/cm<sup>-1</sup>
  - Permittivity of Silicon, ε<sub>Si</sub>= 11.7
  - Permittivity of Silicon Dioxide,  $\varepsilon_{ox}$  =3.97
  - Oxide Layer Thickness, t<sub>ox</sub> = 50x10<sup>-7</sup>
  - The transistor W = 20 μm and L= 0.5 μm
  - The mobility is 500 cm<sup>2</sup>/V
  - a) Determine the threshold voltage,  $V_{tn0}$ , of the transistor with no source-body bias if q = 1.6 x  $10^{-19}$  C,  $n_i$  = 1.5 x  $10^{10}$  and kT/q = 26 mV @  $300^{\circ}$ K.
  - b) Calculate the drain current for an NMOS transistor operating with  $V_{GS} = 5 \text{ V}$  and  $V_{DS} = 3 \text{ V}$  if  $V_{TN} = 1 \text{ V}$  and  $K_n = 1 \text{ mA/V}^2$ . What is the W/L ratio of this device if  $K'_n = 40 \text{ } \mu\text{A/V}^2$ ? What is W if  $L = 0.35 \text{ } \mu\text{m}$ ?
- 3- Design a resistive load inverter with R =1k  $\Omega$ , Such that  $V_{OL}$ =0.6  $V_{\odot}$ .

When nMOS drive transistor has the following parameters:  $V_{DD}$ =5.0 V,  $V_{tno}$ =1.0 V,  $\gamma$ =0.0 V<sup>0.5</sup>,  $\lambda$ =0.0V<sup>-1</sup>, k'= 22  $\mu$ A/V<sup>2</sup>.

- a) Determine the request aspect ratio, W/L.
- b) Determine V<sub>OH</sub>V<sub>IL</sub>. (Draw Drain current Characteristic curve of NMOS with Load Line to know the operation points of your calculated voltage)
- c) Determine noise Margins NML and NMH. If  $V_{IH}$ =2.45 V
- d) Draw layout and Stick diagram, for W/L.=4 and  $R_{sheet}$  poly=0.2K $\Omega$

$$V_{dd} = 5 V$$

$$R = 1 \text{ k}\Omega$$

$$V_{load}$$

$$V_{load}$$

$$V_{load}$$

$$V_{load}$$

$$V_{load}$$

$$V_{load}$$

$$V_{load}$$

$$V_{load}$$